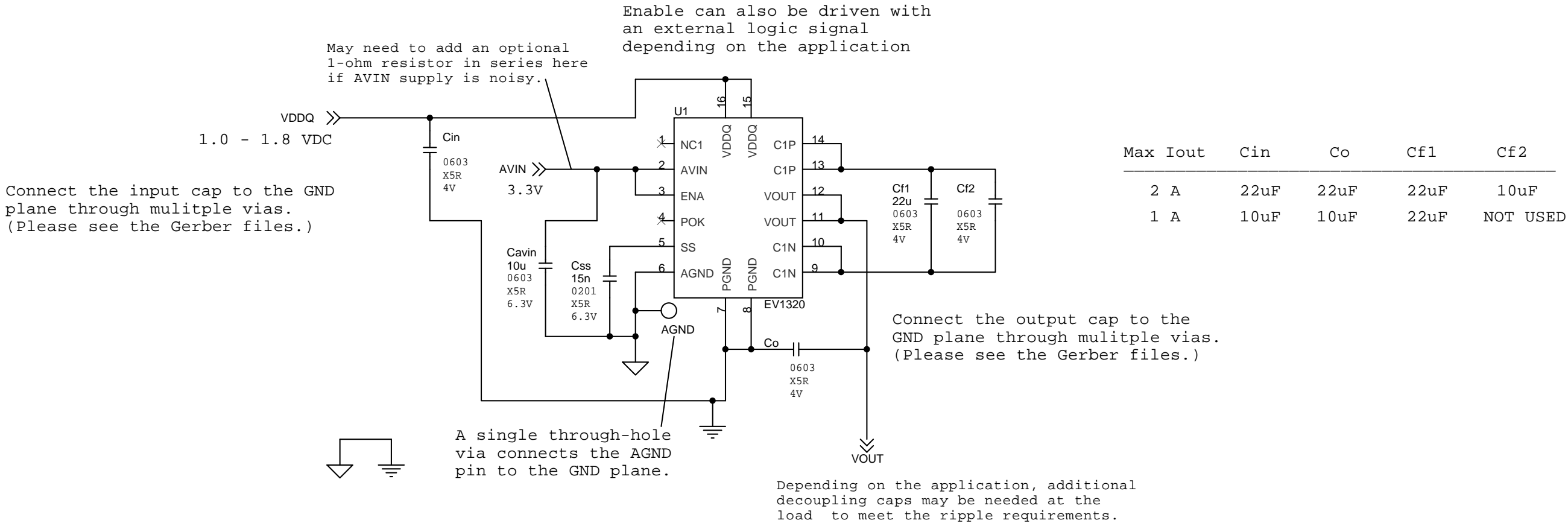


Note: This schematic only shows the critical components and traces for minimum footprint with **ENABLE** tied to **VDDQ**. Other **ENABLE** configurations and the **POK** pin need to be connected and routed according to the customer application.



Schematic 06893  
Gerbers 06894

		CONFIDENTIAL	
Title EV1320 Critical Components Circuit			
Size	Drawing Number		Drawn By AG
Date	3/16/2012		Rev
Sheet		of	