

Revision	Description	Date	Approved

Note: This schematic only shows the critical components and traces for minimum footprint with **ENABLE** tie to Vin. Other **ENABLE** configurations, **POK**, and **SYNC** signals need to be connected and routed according to the customer application.

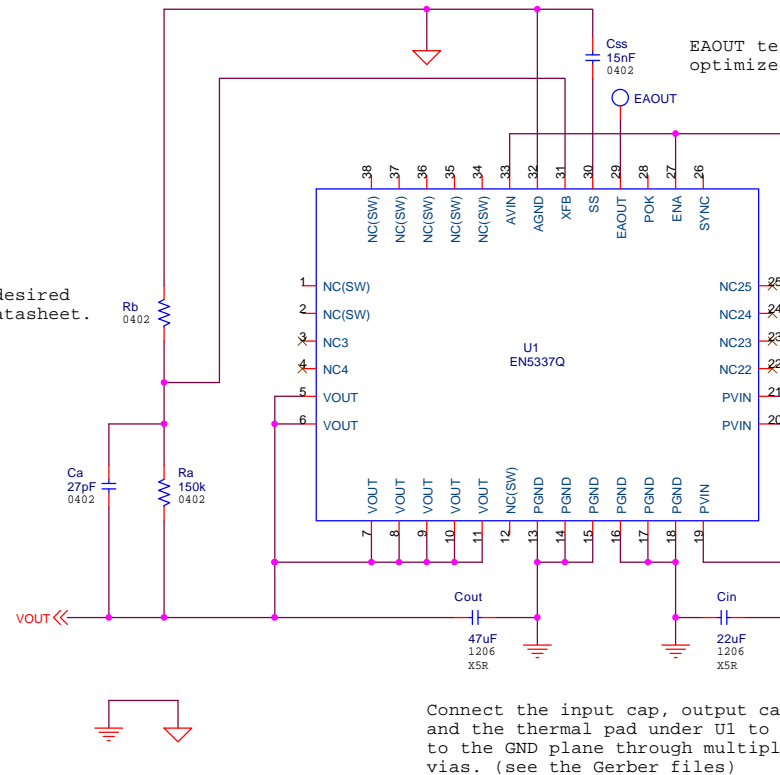
A single through-hole via connects the AGND pin to the GND plane.

EAOUT test point is used to optimize the control loop if needed.

Enable can also be driven with an external logic signal depending on the application


Rb will be set by the desired Vout. Please see the datasheet.

VIN = 2.375 - 5.5 VDC



Connect the input cap, output cap and the thermal pad under U1 to the GND plane through multiple vias. (see the Gerber files)

Schematic 03017
Gerbers 03018

			CONFIDENTIAL		
Title EN5337QI Critical Components Circuit for Minimum Footprint					
Size	Drawing Number 03017			Drawn By C. Romano	Rev
Date	January 28, 2009			Sheet	of